

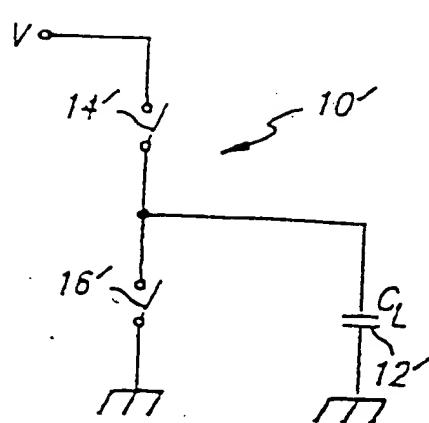
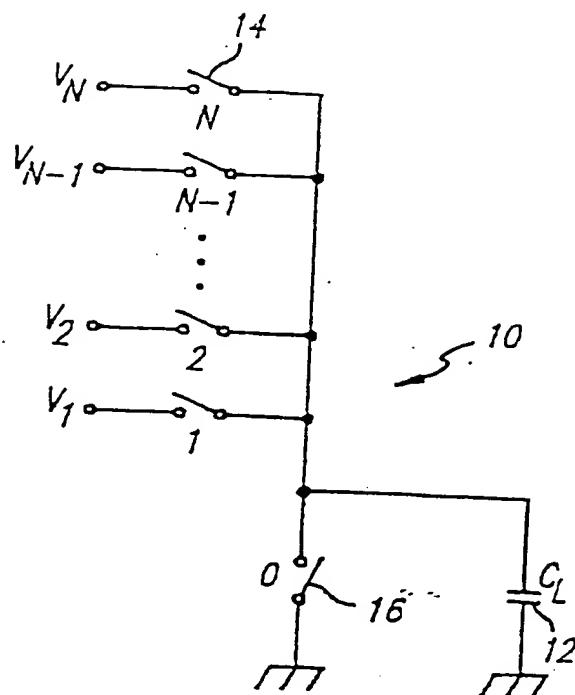
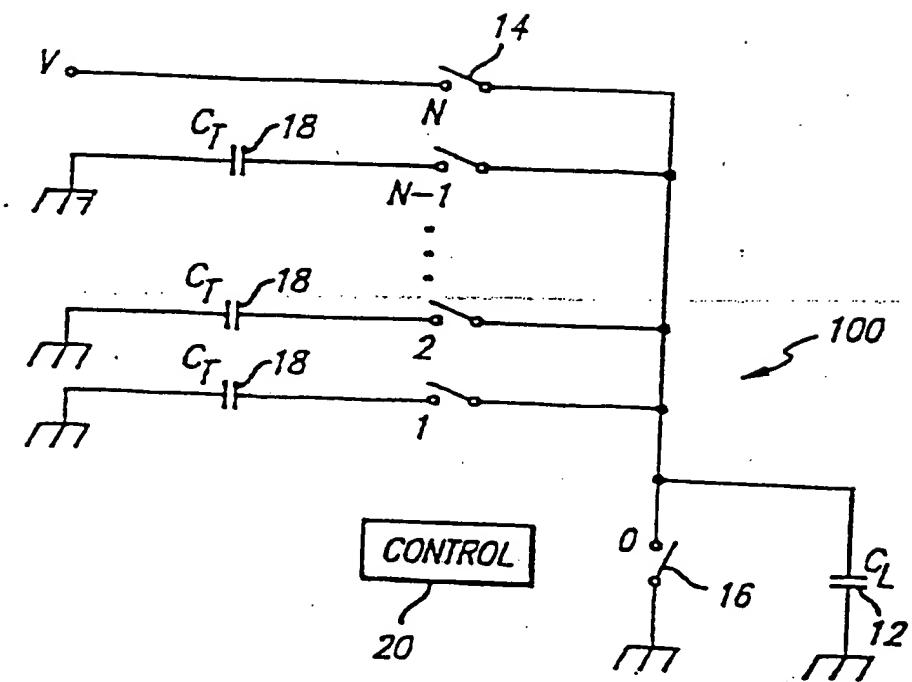
FIG. 1 PRIOR ARTFIG. 2 PRIOR ART

FIG. 3



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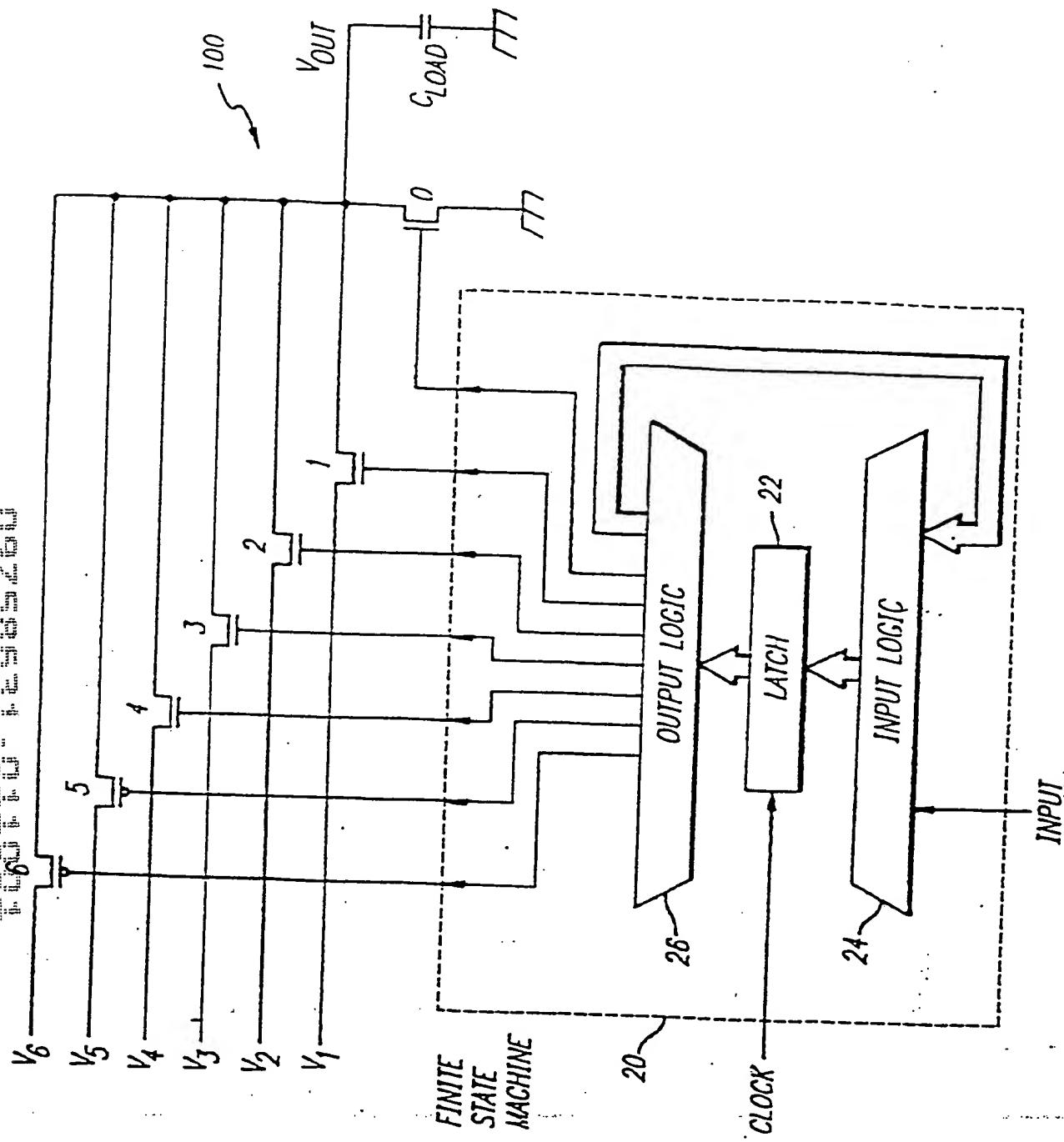


FIG. 4

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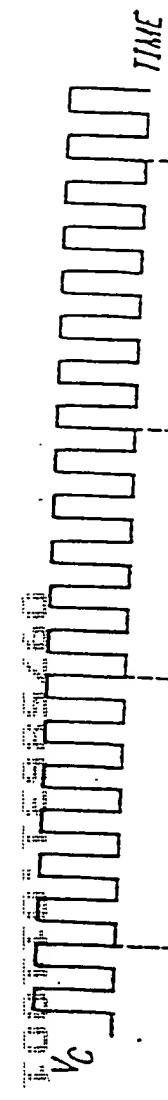


FIG. 5(b) INPUT V_i

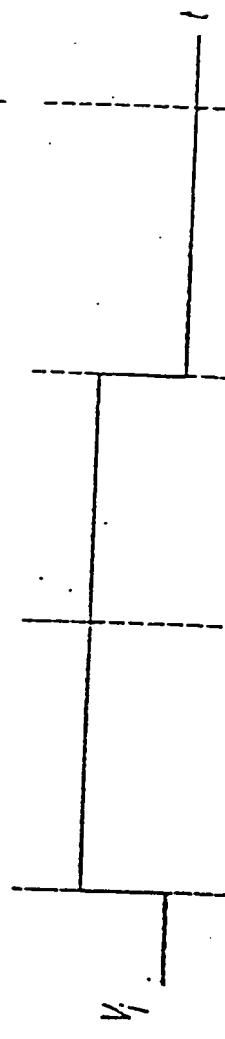


FIG. 5(c) SWITCH #0 CONTROL V_0

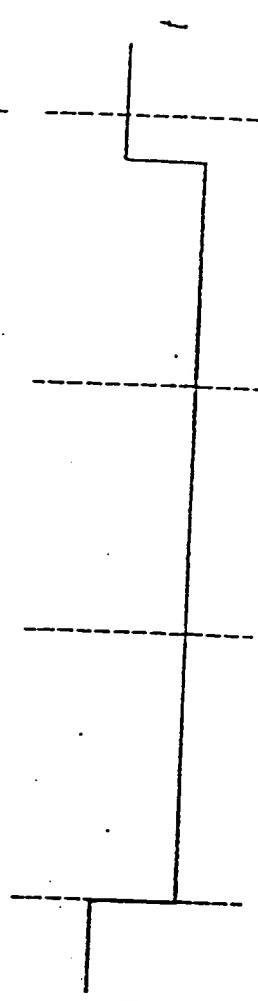


FIG. 5(d) SWITCH #1 CONTROL V_1

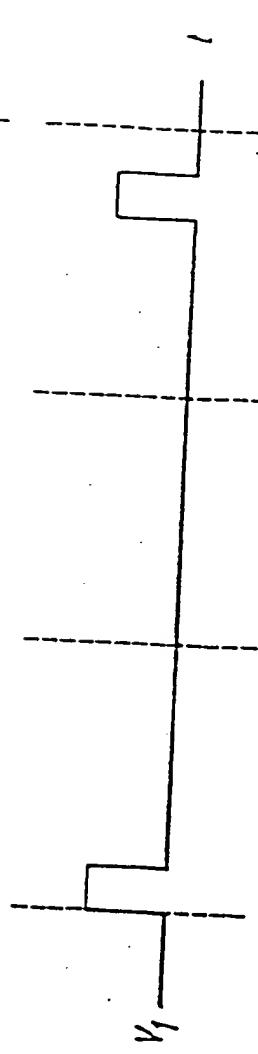
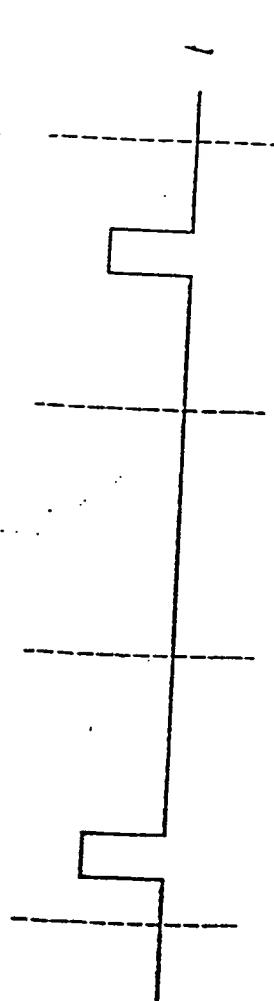
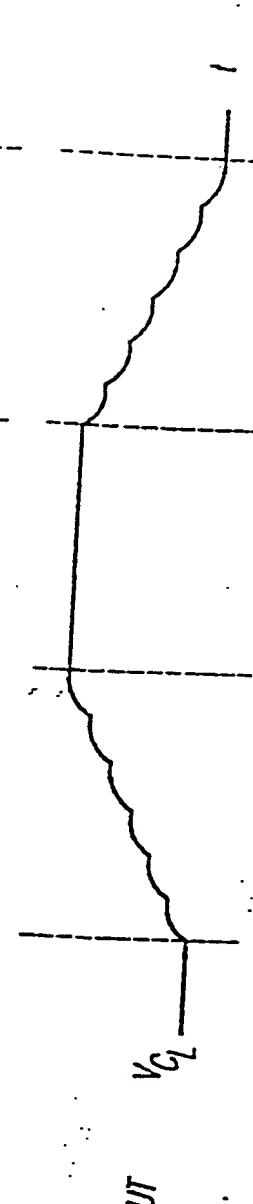
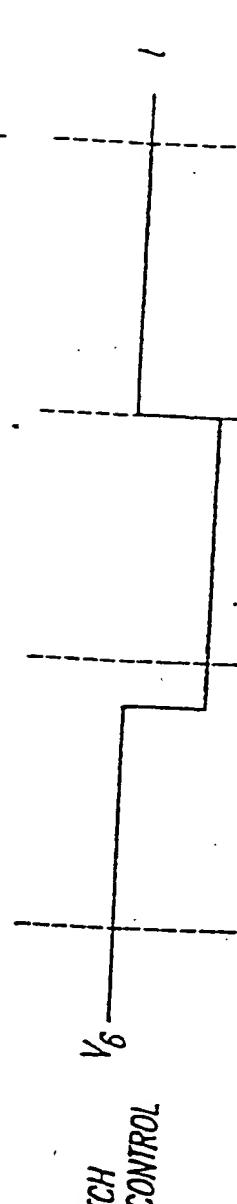
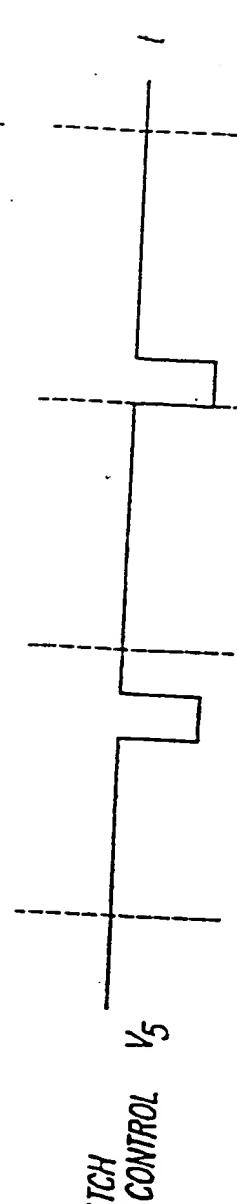
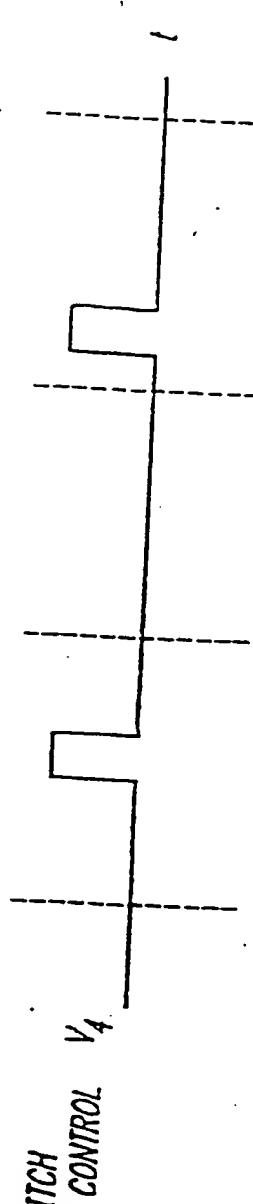
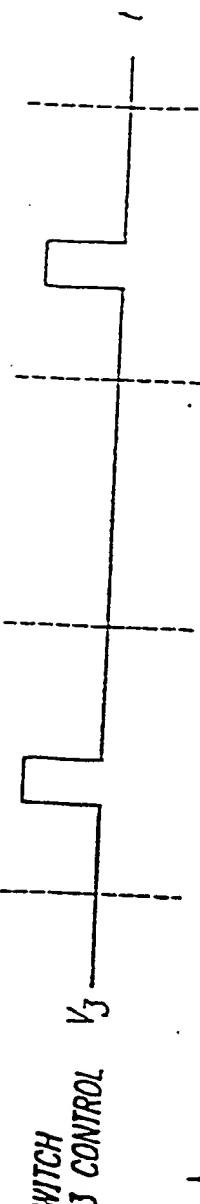


FIG. 5(e) SWITCH #2 CONTROL V_2



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